Ministry of Higher Education

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Communication Engineering Department



Development Of Serial Peripheral Interface Using Field Programmable Gate Array

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REFERENCES

ALTERA, "Character Display for DE2-Series Boards", Altera Corporation, 2011.

ALTERA, "Cyclone II Device Handbook", Altera Corporation, vol 1, 2007.

ALTERA, "Quartus II handbook", Altera Corporation, vol 11, 2012.

ALTERA, "Quick Start Guide for Quartus II Software", Altera Corporation, 2007.

ALTERA, "Quartus II Software tools. Altera Corporation", 2010. Available from World Wide Web:

http://www.ALTERA.com/literature/manual/intro_to_quartus2.pdf

ALTERA, "Quartus II Introduction using Schematic Design", Altera Corporation, 2008.

ALTERA, "Technologies, DE2 User Manual", Altera Terasic Corporation, 2010.

- AHMED, S. Z., SASSATELLI, G., TORRES, L., ROUGE, X, & L, "Survey of New Trends in Industry for Programmable Hardware: FPGAs, MPPAs, MPSoCs, Structured ASICs, eFPGAs and New Wave of Innovation in FPGAs", Aug. 31 2010-Sept. 2 2010) FPLInternational Conference, 2010.
- ASHMANSKAS, W., HANSEN, S., KIPER, T., & PETERSON, D, "FPGA-Based Instrumentation for the Fermilab Antiproton Source",16-20 May 2005, the Particle Accelerator Conference, Proceedings of the PAC 2005.
- AWAD, M, "FPGA supercomputing platforms: A survey", Aug. 31 2009-Sept. 2 2009, the Field Programmable Logic and Applications. International Conference,
- BACCIARELLI, L., LUCIA, G., SAPONARA, S., FANUCCI, L., & FORLITI, M,"Design, testing and prototyping of a software programmable I2C/SPI IP on AMBA bus", (2006, 0-0 0), the Research in Microelectronics and Electronics, Ph. D, 2009.
- B. FLETCHER, "FPGA embedded processors Revealing True System Performance", Embedded Systems Conference, San Francisco, ETP-367, 2005.